

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7324	(315/247,246,291,307,224,225).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/08/03 07:54
L2	1623	1 and (buck boost flyback DC?DC)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/08/03 07:56
L3	1031	2 and (((detect\$4 sens\$4 monitor\$4) near7 (input power main source battery supply DC))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/08/03 07:57
L4	155	3 and (((duty near4 (ratio cycle))) (pulse near3 (width modulat\$4))) same (switch\$4 transistor) same constant)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/08/03 08:01
L5	2	4 and (((duty near4 (ratio cycle))) (pulse near3 (width modulat\$4))) same (microcontroller microprocessor microcomputer) same instruction)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/08/03 09:13
L6	2	4 and (((duty near4 (ratio cycle))) (pulse near3 (width modulat\$4))) same (microcontroller microprocessor microcomputer) same instruction\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/08/03 09:14
L7	66	(buck boost flyback) and LED\$2 and (((duty near4 (ratio cycle))) (pulse near3 (width modulat\$4))) same (microcontroller microprocessor microcomputer) same stor\$4 same instruction\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/08/03 09:19
L8	2	("6864644").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2008/08/03 09:24

8/3/08 9:25:44 AM